



FORM 1449 U.S. Department of Commerce Patent and Trademark Office	Attorney Docket No. 1406/163	Serial No. 10/650,493
List of Documents Cited by Applicant		
		Applicant(s): Doerrer et al.
		Filing Date: August 28, 2003 Group 2819

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing date if Appropriate
<i>Py</i>	1.	4,251,803	2/17/1981	Debord et al.			
	2.	5,117,234	05/26/1992	Shizawa			
	3.	5,550,544	8/27/1996	Sakiyama et al.			
	4.	5,805,093	9/08/1998	Heikkilä et al.			
	5.	5,896,101	4/20/1999	Melanson			
	6.	5,907,299	5/25/1999	Green et al.			
	7.	5,990,814	11/23/1999	Croman et al.			
<i>✓</i>	8.	6,331,833 B1	12/18/2001	Naviasky et al.			
<i>✓</i>	9.	6,426,714 B1	07/30/2002	Ruha et al.			

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Name of Patentee or Applicant	Translation Yes No
<i>m</i>	10.	WO 01/93430	12/06/2001	SE	SPIREA AB	Yes
<i>m</i>	11.	WO 02/09292	1/31/2002	US	The Regents of the University of California	Yes



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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)			
	12.	European Search Report, June 3, 2004.	
	13.	Benabes et al., "A methodology for designing continuous-time sigma-delta modulators," <u>IEEE European Design and Test Conference</u> , 1997. (ABSTRACT) (no month)	
	14.	Cherry et al., "Continuous-Time Delta-Sigma Modulator for High Speed A/D Conversion," <u>Kluwar Academic Publisher</u> , pp. 75-103, 2000. (no month)	
	15.	Gao et al, "Excess loop delay effects in continuous-time delta-sigma modulators and the compensation solution," <u>Circuits and Systems</u> , 1997. (ABSTRACT) (no month)	
	16.	Hernandez, L., "Continuous-time sigma-delta modulators with reduced timing jitter sensitivity based on time delays," <u>Electronics Letters</u> , July 10, 2003. (ABSTRACT)	
	17.	Oliaei et al., "Jitter Effects in Continuous-Time Modulators with Delayed Return-to-Zero Feedback," <u>Electronics, Circuits and Systems</u> , pp. 351-354, 1998. (no month)	
✓	18.	Ramesh et al, "Sigma delta analog to digital converters with adaptive quantization," <u>Circuits and Systems</u> , 1997. (ABSTRACT) (no month)	

EXAMINER

DATE CONSIDERED

5/10/08

*Examiner Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.